

Job Title - SOFTWARE DEVELOPMENT ENGINEER

Job duties: Identification, diagnosis, and correction of characteristics of the FEOL (front end of the line) or BEOL (back end of the line) process or layout which limit product yield and/or performance. Working knowledge of Integration modules including STI isolation, gate, spacer, salicide and stress engineering; or process modules used in BEOL process including deposition, lithography, etch and CMP technologies. Design of experiments for FEOL or BEOL process module improvements. Undertake statistical analysis of manufacturing data and simulation. Determining design rule specifications. Use statistical data analysis techniques (t-test, linear regression, ANOVA) and statistical analysis software tools (Splus, SAS, RS1, JMP, Matlab).

Education/experience requirements: The minimum requirement for this position is a master's degree in computer science, electrical/electronic engineering + one-year experience as a Yield Enhancement Engineer or in a closely related field. The position requires working knowledge of sequence and purpose of typical CMOS semiconductor manufacturing processing steps.

Job site/interview PDF Solutions- Richardson, TX– Salaried/full time position

Interested applicants please send in your resume and refer to Job Code TX-0010718