# PDF/SOLUTIONS"

## Position Title: Software EDA Engineer -JC:0620-00053

Position Location: Desenzano della Garda, Italy

#### Relocation: Not Available

PDF Solutions, Inc. (NASDAQ: PDFS) is a leading provider of yield improvement technologies, services, systems and analytics for the IC manufacturing process life cycle. Headquartered in Santa Clara, Calif., PDF Solutions operates worldwide with additional offices in China, Europe, Japan, Korea and Taiwan.

**Position Summary:** PDF is seeking an experienced Software Engineer to join the R&D team, who is fusing innovative software approaches in a rapidly changing environment using the lasted software engineering technologies and processes to achieve superior results in the field of electronic design automation (EDA). A Characterization Vehicle, or CV, is a core component of PDF's methodology. The CV-System R&D Software Engineer develops physical layout systems and environments to support Characterization Vehicle (CV) ™ test-chip physical layout and verification. This role provides you the opportunity to enhance the efficiency of building CVs. A primary goal of this effort is automating the creation of layout, the physical verification of layout (LVS/DRC) and the documentation and test program generation. This will involve significant software development, support of in-house DOE (Design of Experiment) entry tools, support of in-house layout and verification tools within the Cadence Virtuoso, Calibre, C++, sh and Ruby environments. Support of scripts and tools to summarize and capture output from EDA tools. Support writing and maintaining internal web based documentation system (Ruby on Rails). You will work within a highly motivated, fast-moving team. You will quickly ramp up on the existing code-base, integrate new features, provide maintenance and support, and work with other groups.

## **Qualifications and Skills**

- Master's degree or higher in Computer Science, Computer Engineering, Electrical Engineering or equivalent discipline
- 3+ years of experience with UNIX/Linux knowledge and operation
- Minimum of 3 years of Software Development
- Fluency and demonstrated proficiency in Object-Oriented Programming (OOP) paradigms (C++ preferred)
- Proficiency in an interpreted scripting language (Ruby preferred)
- Excellent computer skills (UNIX/Linux)
- Excellent written and oral communication skills and the ability to work across engineering teams
- Highly professional, self-motivated and self-managed
- Comfortable with fast-paced, iterative, agile design cycles
- Exceptional skills in problem solving

# Job desirables

Beyond the above skills, however, we have found that successful candidates for this job can come from a variety of backgrounds. Therefore, in addition to the above skills, the candidate should demonstrate their additional proficiency in at least two of the following areas:

- Experience with EDA tools (Virtuosso, Thunder, Laker, etc.) and EDA software Development
- Basic understanding of chip design and verification flow (layout, LVS, DRC ...)
- Demonstrated ability to generate and maintain technical documentation
- IC design or manufacturing associated experience
- Cross Platform GUI development experience with Qt toolkit
- Software development of large software system built on top of physical layout environment (MENTOR, CADENCE, SYNOPSYS, etc., CADENCE (SKILL) preferred)
- Web application development experience (Ruby On Rails preferred)
- Experience with databases e.g. MySQL, PostgreSQL